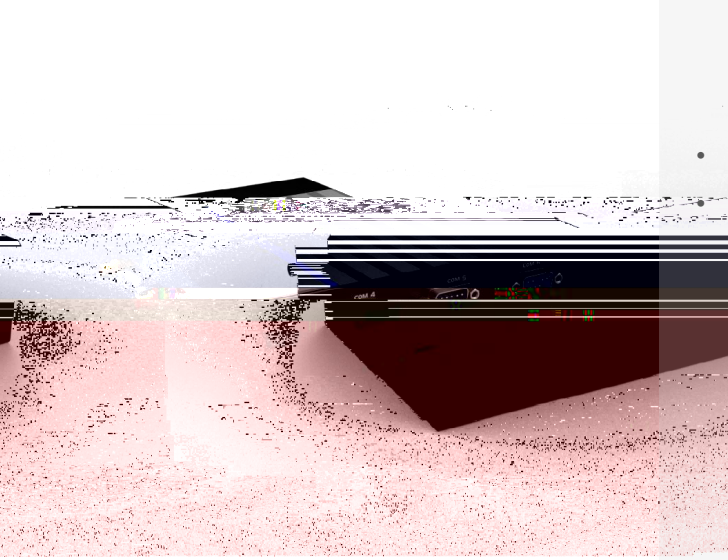


ARM



- Rockchip RK J SoC
x Cortex A x Cortex A

| | | | | | | | | |
|--------------|------|----|----|---|-----|------|-----|------|
| JEA E I A HW | RK J | GB | GB | M | Yes | USB | No | DC V |
| JEA E I A HT | RK J | GB | GB | M | Yes | USB | Yes | DC V |
| JEA E I B HA | RK | GB | GB | G | Yes | PCIe | Yes | DC V |
| JEA E I B ML | RK | GB | GB | M | No | SATA | No | DC V |

DB COM CAN COM RS COM RS

ETX@WJ3

| | | | | |
|--|-----|---------|------|---|
| | AC | V to DC | V | A |
| | CCC | V | A | |
| | | GHz | WiFi | |

| | | | | |
|--|----|---------|-----------|----------------------------|
| | AC | V to DC | V | A |
| | | TOPS | W Typical | Integrated GB LPDDR x MT s |
| | KT | GM | S H M | SATA Gbps |
| | KT | GM | S H M | SATA Gbps |
| | TS | GMTS | T VS M | SATA Gbps |
| | TS | GMTS | T VS M | SATA Gbps |
| | EM | CN | LTE Cat | |
| | EM | G | LTE Cat | eSIM |
| | RM | U CNV | G Sub MT | J 1 |